



**MANIPAL**  
INSTITUTE OF TECHNOLOGY  
*(A constituent unit of MAHE, Manipal)*



**MIT-MAHE  
and  
Entuple Technologies**

**Cordially invite you to**

**Training Program  
on**

**Two-Day Hands-on Training on OrCaD Tools**

**Organized by  
Department of Mechatronics  
MIT, Manipal**

**18<sup>th</sup> -19<sup>th</sup> March 2025 | 09.30 AM onwards  
PG Lab, AB1, GF, MIT Manipal**

**DAY 1 – MARCH 18, 2025**

## **Electronic Circuit Design- Hands On**



By

**Mr. Kamlesh Kumar**  
Application Engineer,  
Entuple Technologies

**The highlights of the session are:**

- Introduction to Electronic Product Design Cycle
- Design Entry (Schematic): Design Example – Hands On
- Predict the behavior of your circuit: Circuit Simulation – Hands On.

**Venue**

**PG LAB Ground Floor AB1,**  
**Timings: 09.30 AM TO 12.30 PM**  
**and 2.00 PM TO 4:30 PM**

**Organized by**

Department of Mechatronics, MIT, Manipal Organizing  
**Chairman: Dr. D V Kamath, HoD**

**Convener**

**Dr. Vijay Mohan, Dr. Niraj Kumar Dewangan**

**Co-Convener**

**Dr. Nikhil Pachauri, Dr. Jibitesh Kumar Panda**  
Dept. of Mechatronics, MIT, Manipal





## DAY 2 – MARCH 19, 2025

### Session1: PSpice model Editor

The highlights of the session are:

- Generating New parts with PSpice model Editor
- Generate the BOM (Bill of Material)
- Hands-on simulation for Different circuits

### Session2: PCB and Layout Design

The highlights of the session are:

- Introduction to PCB and Layout Design Process- Hands On
- Route the Layout
- Generate Fabrication Files/Data for Layout

By

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Application Engineer,  
Entuple Technologies

Venue

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